PCI-X to PMC Adapter



Revision A

General Standards Corporation 8302A Whitesburg Drive Huntsville, AL 35802 Phone: (256) 880-8787 Fax: (256) 880-8788 URL: <u>www.generalstandards.com</u> E-mail: <u>support@generalstandards.com</u>

PREFACE

General Standards Corporation Copyright (C) 2002 **General Standards Corporation**

Additional copies of this manual or other literature may be obtained from:

General Standards Corporation 8302A Whitesburg Dr. Huntsville, Alabama 35802 Tele: (256) 880-8787 FAX: (256) 880-8788 E-mail: <u>support@generalstandards.com</u>

The information in this document is subject to change without notice.

General Standards Corporation makes no warranty of any kind with regard to this material, including, but not limited to, the implied warranties of merchantability and fitness for a particular purpose. Although extensive editing and reviews are performed before release to ECO control, General Standards Corporation assumes no responsibility for any errors that may exist in this document. No commitment is made to update or keep current the information contained in this document.

General Standards Corporation does not assume any liability arising out of the application or use of any product or circuit described herein, nor is any license conveyed under any patent rights or any rights of others.

General Standards Corporation assumes no responsibility for any consequences resulting from omissions or errors in this manual or from the use of information contained herein.

General Standards Corporation reserves the right to make any changes, without notice, to this product to improve reliability, performance, function, or design.

All rights reserved

INTRODUCTION

1.0 FUNCTIONAL DESCRIPTION

The PCI-X to PMC Adapter Board is used to operate a PMC board in a PCI-X or a PCI Bus. The Adapter has a 64Bit Bus and can be installed into a 64Bit or 32Bit PCI or PCI-X Bus. The adapter can be setup via jumpers to operate the primary side at PCI-X operational speeds as high as 133MHz or the adapter can operate in a legacy 33MHz PCI bus.

The PMC site or secondary bus can operate with 64Bit or 32Bit PMC cards. The operational clock speed of the PMC Bus can be selected via jumpers to have a system clock as high as 133 MHz PCI-X or as low as 33MHz Legacy PCI. The signaling environment of the PMC site can also be selected via jumpers to operate at 5V or 3.3V levels.

1.1 OPERATIONAL CHARACTERISTICS

Due to the multiple selectable features of the adapter, almost any PMC card can be used in almost any system without suffering performance issues.

Signaling Environment Adaptation

The PMC (Secondary Bus) Signaling is jumper selectable and the PCI-X (Primary Bus) Signaling is taken from the host computer. This allows a PMC card with signaling different from the host computer to be used together.

32Bit to 64Bit Data Packing

A PMC card with 32Bit Bus can operate in a 64Bit Bus but can only transfer data at 32Bits. With this adapter, when a block of data is transferred from a 32Bit PMC Card, the data is packed into 64bit format when it is transferred over the PCI-X Bus. This takes only half as much bus bandwidth as it would without the adapter.

PCI-X to PCI Conversion

The main operational mode of the adapter is to convert between bus Clock speeds without requiring the faster bus to throttle down the slowest speed. The main purpose of this is to use a slower PMC card to transfer data to a faster PCI/PCI-X card and still using the higher system bandwidth.

PCI to PMC Conversion

The adapter allows the use of a PMC card in a PCI environment. This allows the use of PMC boards with functions or special features that do not exist in PCI.

JUMPER SETTINGS

2.1 User Configurable Jumpers

• Do not change Jumpers Settings with POWER ON to the Host Bus! This may cause damage to your Host Computer and/or the PMC Board..

J1 – PCI Power Requirements

This jumper is used to indicate to the PCI Bus the power requirements for this slot.

| Power Requirements | J1:1-2 | J1:3-4 |
|-----------------------|--------------|---------|
| < 7.5 Watts (Default) | Shorted | Shorted |
| < 15 Watts | Open | Shorted |
| < 25 Watts | Shorted Open | |

Add 1.5Watts for the PCI-X Adapter to your PMC power requirements to determine the value to use to determine jumper settings.

J7 – SECONDARY SIGINALING SELECT J8 – PMC VIO SELECT

These jumpers are used to indicate to the signaling level between the PCI-X Bridge and the PMC site. J7 selects the signaling levels the PCI-X Bridge to use to the PMC site. J8 selects the VIO Voltage to the PMC site that the PMC card will use to determine it's signaling levels. Both J7 and J8 must be set to the same level.

Set this to a level that is compatible with your PMC Card. A PMC card with Universal Signaling will operate in either a 5V or 3.3V Signaling environment.

| PMC Signaling Voltage | J7 | J8 |
|---------------------------|--------------|--------------|
| 5V Signaling * | Short J7:1-2 | Short J8:1-2 |
| 3.3V Signaling (Default)* | Short J7:2-3 | Short J8:2-3 |

• J7 & J8 must be set to the same level.

J9 – PRIMARY SIGINALING SELECT

This jumper is used in conjunction with J11:13-14 to indicate to the PCI-X Bridge the desired signaling to use to the Host PCI Bus. This used for factory testing and should be set to "Use Primary Bus VIO".

- It is rare that be set to anything other than "Use Primary Bus VIO". Consult Factory before changing.
- Damage to Host Computer could occur if J11:13-14 is shorted when any pins on J9 are shorted.

| PCI-X Signaling Voltage | J 9 | J11:13-14 |
|---------------------------------|--------------|-----------|
| Use Primary Bus VIO (Default) * | All OPEN | Shorted |
| 5V Signaling * | Short J9:1-2 | Open |
| 3.3V Signaling * | Short J9:2-3 | Open |

J10 – SECONDARY BUS CLOCK SPEED (PMC BUS CLOCK SPEED)

This jumper is used to select the maximum PMC Bus Clock Speed.

| PMC Bus Speed | J10:1-2 | J10:3-4 | J10:5-6 | J10:7-8 | J10:9-10 | J10:11-12 |
|----------------------|---------|---------|---------|---------|----------|-----------|
| PCI: 33MHz | Open | Open | Shorted | Open | Open | Shorted |
| PCI: 66MHz (Default) | Open | Open | Shorted | Open | Shorted | Open |
| PCI-X: 66MHz | Open | Shorted | Open | Open | Shorted | Open |
| PCI-X: 100MHz * | Open | Open | Open | Open | Open | Open |
| PCI-X: 133MHz * | Open | Open | Open | Open | Open | Open |
| Use PMC PCI-CAP Pin | Shorted | Open | Open | | | |
| Use PMC M66EN Pin | | | | Shorted | Open | Open |

• 100MHz /133 MHz determined by J5:1-2. If J5:1-2 Open = 100 MHz. If J5:1-2 Shorted =133MHz.

J12 – PRIMARY BUS CLOCK SPEED (PCI-X BUS CLOCK SPEED)

This jumper is used to select the maximum PCI-X Bus Clock Speed.

| PCI-X Bus Speed | J12:1-2 | J12:3-4 | J12:5-6 |
|-------------------------|---------|---------|---------|
| PCI: 33MHz | Shorted | Open | Shorted |
| PCI: 66MHz | Shorted | Open | Open |
| PCI-X: 66MHz | Open | Shorted | Open |
| PCI-X: 133MHz (Default) | Open | Open | Open |

2.2 FACTORY RESERVED JUMPERS

J2 – JTAG

Reserved for factory use. The jumper should remain as follows:

| J2: 1-2 (Default) | Shorted |
|--------------------|---------|
| J2: 3-4 (Default) | Open |
| J2: 5-6 (Default) | Open |
| J2: 7-8 (Default) | Open |
| J2: 9-10 (Default) | Open |

J3 – TEST

Reserved for factory use. The jumper should remain as follows:

| J3: 1-2 (Default) | Open |
|--------------------------|------|
| J3: 3-4 (Default) | Open |
| J3: 5-6 (Default) | Open |
| J3: 7-8 (Default) | Open |
| J3: 9-10 (Default) | Open |
| J3: 11-12 (Default) | Open |
| J3: 13-14 (Default) | Open |
| J3: 15-16 (Default) | Open |
| J3: 17-18 (Default) | Open |
| J3: 19-20 (Default) | Open |
| J3: 21-22 (Default) | Open |
| J3: 23-24 (Default) | Open |
| J3: 25-26 (Default) | Open |
| J3: 27-28 (Default) | Open |

J4 – BRIDGE GPIO

Reserved for factory use. The jumper should remain as follows:

| J4: 1-2 (Default) | Open |
|---------------------|------|
| J4: 3-4 (Default) | Open |
| J4: 5-6 (Default) | Open |
| J4: 7-8 (Default) | Open |
| J4: 9-10 (Default) | Open |
| J4: 11-12 (Default) | Open |
| J4: 13-14 (Default) | Open |
| J4: 15-16 (Default) | Open |

J5 – BRIDGE CLOCK CONTROL

Reserved for factory use. The jumper should remain as follows:

| J5: 1-2 (Default) | Open |
|--------------------------|---------|
| J5: 3-4 (Default) | Open |
| J5: 5-6 (Default) | Open |
| J5: 7-8 (Default) | Shorted |
| J5: 9-10 (Default) | Shorted |
| J5: 11-12 (Default) | Shorted |

J6 – HOT SWAP

Reserved for factory use. The jumper should remain as follows:

| J6: 1-2 (Default) | Open |
|--------------------|---------|
| J6: 3-4 (Default) | Shorted |
| J6: 5-6 (Default) | Shorted |
| J6: 7-8 (Default) | Shorted |
| J6: 9-10 (Default) | Shorted |

J11 – BRIDGE STRAPPING OPTIONS

Reserved for factory use. The jumper should remain as follows:

| J11: 1-2 (Default) | Open | | |
|----------------------|-----------------|--|--|
| J11: 3-4 (Default) | Shorted | | |
| J11: 5-6 (Default) | Shorted | | |
| J11: 7-8 (Default) | Shorted | | |
| J11: 9-10 (Default) | Open | | |
| J11: 11-12 (Default) | Shorted | | |
| J11: 13-14 (Default) | *** Shorted *** | | |

*** J11:13-14 must be open if J9 - Primary Signaling Select is NOT set to "Use Primary Bus VIO" ***