

## 16AO16 connector pin assignments

Connector	Signal
J00	OUTPUT 00 LO
	OUTPUT 00 HI
J01	OUTPUT 01 LO
	OUTPUT 01 HI
J02	OUTPUT 02 LO
	OUTPUT 02 HI
J03	OUTPUT 03 LO
	OUTPUT 03 HI
J04	OUTPUT 04 LO
	OUTPUT 04 HI
J05	OUTPUT 05 LO
	OUTPUT 05 HI
J06	OUTPUT 06 LO
	OUTPUT 06 HI
J07	OUTPUT 07 LO
	OUTPUT 07 HI
J08	OUTPUT 08 LO
	OUTPUT 08 HI
J09	OUTPUT 09 LO
	OUTPUT 09 HI
J10	OUTPUT 10 LO
	OUTPUT 10 HI
J11	OUTPUT 11 LO
	OUTPUT 11 HI
J12	OUTPUT 12 LO
	OUTPUT 12 HI
J13	OUTPUT 13 LO
	OUTPUT 13 HI
J14	OUTPUT 14 LO
	OUTPUT 14 HI
J15	OUTPUT 15 LO
	OUTPUT 15 HI
J16	TRIG IN LO *
	TRIG IN HI *
J17	TRIG OUT LO *
	TRIG OUT HI *
J18	DACCLK OUT LO *
	DACCLK OUT HI *
J19	CLOCK IO LO **
	CLOCK IO HI **
J20	VTEST RTN
	VTEST
J21	OUT RTN
	REM GND SEN

**NOTE:**

The differential analog output configuration is shown. For optional single-ended outputs, OUTPUT XX HI is an output, and OUTPUT XX LO will be connected to output rtn.

\* LVDS differential pairs. In TTL mode, 'HI' pins are signal pins, and 'LO' inputs will be connected to digital return.

\*\* Bidirectional signal.